IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: FRANK W. BRICE, JR., ET AL

FOR: MEMORY MAPPED INPUT/OUTPUT VIRTUALIZATION

INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR §§ 1.56, 1.97 AND 1.98

The Commissioner of Patents
  and Trademarks
P.O. Box 1450
Alexandria, VA 22313-1450

In compliance with the duty to disclose, applicants submit herewith a copy of each
publication, excluding U.S. Patents and U.S. published patent applications, of which those
designated by 37 CFR § 1.56 are aware; the publications are listed on attached form PTO-
1449. The present Information Disclosure Statement shall not be construed as a
representation that a search has been made or that no other art than that identified on the
attached form PTO-1449 exists.

Consideration of this Information Disclosure Statement is respectfully requested,
since the Examiner may consider the information provided herein material to the
patentability of the subject application as defined in 37 CFR § 1.56.

Since this Information Disclosure Statement is being filed within three months of the
filing date of the subject application, no fee or certification under 37 CFR § 1.97 (e) is

required.

However, in the event the Commissioner of Patents deems that any fee is required under 37 CFR §§ 1.16 and 1.17 in connection with this application, applicants' attorneys authorize that such fee be charged to Deposit Account No. 09-0463.

Respectfully submitted,

FRANK W. BRICE, JR., ET AL

CANTOR COLBURN LLP
Applicants' Attorneys
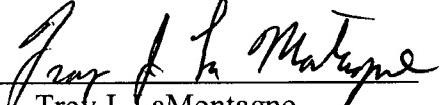
By:_____
Troy J. LaMontagne
Registration No. 47,239
Customer No.   23413

Date: November 25, 2003
Address: 55 Griffin Road South, Bloomfield, Connecticut 06002
Telephone: (860) 286-2929

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
/C.Q/	1	5,721,858	02/24/1998	White et al	395	413	12/12/1995
/C.Q/	2	6,061,757	05/09/2000	Arimilli et al	710	264	11/13/1997
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/C.Q/	5	6,442,635 B1	08/27/2002	Brightman et al	710	268	11/16/1998

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES	NO
/C.Q/	6	JP9069072A	1997-03-11	Japan w/ English Abstract				✔

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

/C.Q/	7	Formal Properties of Recursive Virtual Machine Architectures. Gerald Belpaire* New York University and Nai-Ting Hsu* University of Wisconsin .* Department of Computer Science, Courant Institute of Mathematical Sciences. This work is supported by ERDA under contract E(11-1)-3077. ** Computer Sciences Department and Mathematics Research Center. This work is supported in part by the US Army contract DAHCO4-75-C-0024. pages 89-96 , 11/1975
/C.Q/	8	DAISY: Dynamic Compilation for 100% Architectural Compatibility. Kemal Ebcioglu and Erik R. Altman. IBM Thomas J. Watson Research Center, Yorktown Heights, NY 10598. {kemal,erik}@watson.ibm.com. 1997 ACM 0-89791-901-7/97/0006...$3.50 pages 26-37

EXAMINER /Camquy Truong/	DATE CONSIDERED 09/24/2008

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INFORMATION DISCLOSURE CITATION

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OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Pages, Etc.)*

/C.Q/	9	Emulation - A Useful Tool in the Development of Computer Systems. F.A. Salomon and D.A. Tafuri. Bell Laboratories, Naperville, IL Annual Simulation symposium. pages 55 -71 , 7/1995
/C.Q/	10	IBM Technical Disclosure Bulletin. Vol. 38 No. 07 July 1995. Emulating Memory Mapped File Input/Output on OS/2. pages 505-507

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Form PTO-A820
(also form PTO-1449)

P09C/REV03

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FOREIGN PATENT DOCUMENTS

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/C.Q/	11	Design Challenges of Virtual Networks: Fast, General-Purpose Communication. Alan M. Mainwaring, Computer Science Division, University of California at Berkely. Berkeley, CA 94720-1776 and David E. Culler, Computer Science Division, University of California at Berkeley. Berkeley, CA 94720-1776. 5/99 Atlanta, GA, USA. 1999 ACM 1-58113-100-3/99/0004...$5.00

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